

AMENDMENT TO THE DRAWINGS

Please add new Figures 24(a)-(g), 25, 26A-26H, and 27-29 shown on the attached new sheets of drawings.

REMARKS

I. Amendments to the Specification and Drawings

In this Amendment, Applicants have amended the specification to include drawings and description present in U.S. Patent Nos. 6,034,882 and 6,420,215, which were incorporated by reference in the present application. Applicants have changed the figure numbers and some of the reference numbers for consistency. Applicants are submitting a statement executed by the undersigned attorney stating that these drawings and description were previously incorporated by reference and that this amendment contains no new matter.

II. Objection to the Abstract

In the Office Action, the Abstract was objected to as not being descriptive of the claimed invention. In response to this objection, Applicants have amended the Abstract. In view of this amendment, Applicants respectfully submit that the objection to the Abstract has been overcome.

III. Claim Rejections

Claims 1-11 and 21 were rejected as being anticipated by U.S. Patent No. 6,903,427 to Zhang. Independent Claims 1 and 21 both recite a monolithic three-dimensional write-once memory array. In this Amendment, Applicants have amended independent Claims 1 and 21 to clarify that term. Specifically, Applicants have amended independent Claims 1 and 21 to clarify that the recited monolithic three-dimensional write-once memory array comprises a first conductor, a first memory cell above the first conductor, a second conductor above the first memory cell, and a second memory cell above the second conductor, wherein the second conductor is the only conductor between the first and second memory cells.

In contrast, a memory array in Zhang has *two* conductors between two vertically displaced memory cells — *not* a memory array in which there is *only one* conductor between two

vertically displaced memory cells, as recited in the claims. For example, with reference to Figure 1A of Zhang, between any memory cell from the first memory level and any memory cell from the second memory level, there are *two* conductors. In contrast, the claims recite a memory array in which there is *only one* conductor between two vertically displaced memory cells.

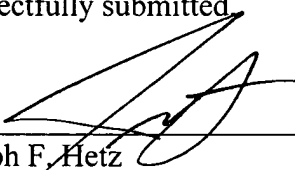
Because Zhang fails to teach the recited memory array, Applicants respectfully request reconsideration and withdrawal of the 35 U.S.C. § 102(e) rejections and submit that this application is in condition for allowance.

IV. Conclusion

In view of the foregoing amendments and remarks, Applicants respectfully submit that this application is in condition for allowance. Reconsideration is respectfully requested. If there are any questions concerning this Amendment, please contact the undersigned attorney at (312) 321-4719.

Dated: December 5, 2007

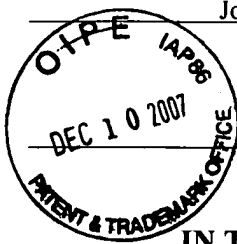
Respectfully submitted,



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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on December 5, 2007
Date of Deposit



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Name of Applicant, Assignee or
Registered Representative

Signature

Our Case No. 10519-114

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------------------------------|---|-----------------|-------|
| In re Application of: |) | | |
| Crowley et al |) | | |
| Serial No.: 10/774,758 |) | Examiner: | Britt |
| Filed: February 9, 2004 |) | Group Art Unit: | 2117 |
| For: System Architecture and Method |) | | |
| for Three-Dimensional Memory |) | | |

STATEMENT UNDER 37 C.F.R. § 1.57(f)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

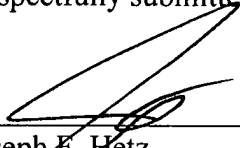
Dear Sir:

In the accompanying Amendment, Applicants are adding several paragraphs and figures.
Pursuant to 37 C.F.R. § 1.57(f), Applicants state that the material being inserted is material previously incorporated by reference and that the Amendment contains no new matter.

Dated: December 5, 2007

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